

DAYANANDA SAGAR COLLEGE OF ENGINEERING
Shavige Malleshwara Hills, Kumaraswamy Layout
Bangalore – 56078

Advertisement for fill up Project Position

Applications are invited from Indian nationals for project position as per the details given below for the research project under the Principal Investigator **Dr. Jamuna .S**, Professor, and Co-Principal Investigators **1.Dr.Dinesha P.** Professor, **2. Prof. K.P.Shashikala**, Department of Electronics and Communication, Dayananda Sagar College of Engineering, Bangalore, Karnataka.

1. **Title of the Project:** “Design and development of fault tolerant technique for Reconfigurable Computing Applications.”
2. **Sponsor of the Project:** Defence Research & Development Organization (DRDO), Government of India, Project ID: ERIP/ER/DG-Med&Cos/990916502/M/01/1659, Dated: April 26, 2017.
3. **Project Position and number:** Junior Research Fellow – 01
4. **Qualification:** M.Tech /M.E with specialization in VLSI and Embedded systems Design.
5. **Technical Skill:** Knowledge about VLSI design verification and testing, programming using VHDL/Verilog and FPGA design flow.
6. **Duration:** 24 months.
7. **Fellowship:** Rs.25000/- per month [as per DRDO - JRF Norms]
8. **Job Description and Requirement:** Project is about developing Fault Tolerant (FT) technique for the computing systems mapped on high-end FPGAs. Fault detection, diagnosis and fault avoidance mechanism to be integrated and applied on to the applications configured on FPGAs.

Note:

1. Candidates before applying for the interview should ensure that they are eligible.
2. Candidates desiring to appear for the interview should submit their application with the relevant documents to the office of Principal/Co-Investigator at the time of interview.
3. Preference will be given to candidates planning to pursue Ph.D in the same research area.

CV should be sent to below e-mail address (“Application for JRF” as Subject name)

E-mail: js.jamuna@gmail.com

Copy to:

1. Principal, DSCE (ppl-dsce@dayanandasagar.edu)
2. HR Manager, DSCE (hr@dayanandasagar.com)