

**DAYANANDA SAGAR INSTITUTE OF TECHNOLOGY**  
**CLASS TIME TABLE 2023 - 2024 (EVEN SEMESTER)**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**IV SEMESTER**

ROOM NO: DB 307

DAY/TIME	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00 - 3:00	3:00 - 4:00	4:00 - 5:00
MONDAY	IA	EC	WLC	IC	L U N C H  B R E A K	PCB LAB (304)		
TUESDAY		IA	WLC LAB(306)			EMBEDDED C LAB(304)		
WEDNESDAY	EC	WLC	IC	PCB		IA LAB(306)		
THURSDAY	PCB	EC	IA LAB(306)			WLC LAB(306)		
FRIDAY	WLC	IA	EMBEDDED C LAB(304)			PCB LAB (304)/(306)		
SATURDAY								

  
SIGNATURE OF HOD



  
SIGNATURE OF PRINCIPAL

CODE	SUBJECT NAME	INITIALS	LECTURERS
20EC41P	PCB DESIGN AND FABRICATION	SS	SANDHYA S
20EC42P	WIRELESS COMMUNICATION	SUM	SUDHAMANI
20EC43P	EMBEDDED C LAB	MR	MUYEEN RESHMA
20EC44P	IA LAB	SN	SHARADHA N
20EC45T	INDIAN CONSTITUTION	MR	MUYEEN RESHMA