

DAYANANDA SAGAR INSTITUTE OF TECHNOLOGY
CLASS TIME TABLE 2023 - 2024 (ODD SEMESTER)
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
I SEMESTER

ROOM NO: DB 301

DAY/TIME	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00	4:00-5:00
MONDAY	FEEE LAB (B2) DB 310		DE	FEEE	L U N C H B R E A K	SAT LAB (B2)		
TUESDAY		YOGA	DE	DE		FEEE LAB (B1) DB306/ SAT LAB (B2)		
WEDNESDAY		EVS	DE	STUDY HOUR		SAT LAB (B1)/ CAEG (B2)		
THURSDAY			DE	DE		CAEG (B1) / FEEE LAB (B2) DB 306		
FRIDAY	EVS	FEEE LAB (B1) DB 310 / CAEG (B2) A-LAB				SAT LAB (B1)		
SATURDAY		CAEG (B1)				CAEG (B1)		


SIGNATURE OF HOD




SIGNATURE OF PRINCIPAL

CODE	SUBJECT NAME	INITIALS	LECTURERS
20EC11T	DIGITAL ELECTRONICS	SS	SANDHIYA.S
20EC02P	STATISTICS & ANALYTICS LAB	RM	RADHA M
20ME02P	COMPUTER AIDED ENGG. GRAPHICS	BM	BHARATH M
20EC01P	FEEE Lab	CV	CHANDRAKALA V
20AU01T	ENVIRONMENTAL SUSTAINABILITY	UA	USHARANI A

DAYANANDA SAGAR INSTITUTE OF TECHNOLOGY(POLYTECHNIC)

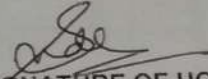
CLASS TIME TABLE 2023 - 2024 (ODD SEMESTER)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

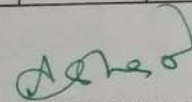
III SEMESTER

ROOM NO: DB 302

DAY/TIME	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00 - 3:00	3:00 - 4:00	4:00 - 5:00
MONDAY	LDV	AE LAB (306)/(304)			L B U R N E C A H K	EMTT LAB (306)		
TUESDAY	CS	AE LAB (306)				LDV LAB (304)		
WEDNESDAY		AE	KANNADA	CS		EMTT LAB (306)		
THURSDAY	EMTT	Communication System lab(306)				LDV LAB (304)		
FRIDAY	EMTT	LDV	KANNADA	AE		Communication System lab(306)		
SATURDAY			BALAKE KANNADA					


SIGNATURE OF HOD




SIGNATURE OF PRINCIPAL

CODE	SUBJECT NAME	INITIALS	LECTURERS
20EC31P	Analog Electronics	SUM	SUDHAMANI
20EC32P	Logic Design using verilog	MR	MUYEEN RESHMA
20EC33P	Communication systems	CV	CHANDRAKALA V
20EC34P	Electronic measurements & Testing	SS	SANDHYA.S
20KA31T	Kannada 2	SV	SANDHYA. V